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### **Amendments to the Claims**

A listing of the entire set of pending claims (including amendments to the claims, if any) is submitted herewith per 37 CFR 1.121. This listing of claims will replace all prior versions, and listings, of claims in the application.

#### **Listing of Claims:**

1. (Currently Amended) A digital video-processing unit for processing an input signal to an output data signal for a display unit, said video-processing unit comprising
  - a processor for processing the input signal to the output data signal, the processing including a first resizing and a second resizing,
  - memory means for storing the input signal prior to supply to said processor, and
  - a memory manager coupled with the processor and the memory means, the memory manager being arranged to transfer the input data signals to the memory means and to transfer the stored input data signal from the memory means to the processor for the first resizing, respectively, wherein an output of the processor is coupled to an input of the memory manager, wherein the memory manager is further arranged to transfer the output data signal from the processor to the memory means for storing the output data

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signal, and to transfer the stored output data signal from the memory means to the display unit, and wherein the memory manager is further arranged to transfer the stored ~~data~~-output data signal from the memory means to the processor, and said processor is further arranged to execute ~~a further operation~~ the second resizing on the stored data output signal, wherein said processor and said memory manager are arranged to execute different processes in time multiplex.

2-3. (Cancelled).

4. (Previously Presented) The digital video-processing unit of claim 1, wherein said processor, said memory manager and the memory means are comprised on a single integrated circuit.

5. (Previously Presented) The digital video-processing unit of claim 1, wherein said processor is arranged to resize an image represented by said input data signal.

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6. (Previously Presented) The digital video-processing unit of claim 1, wherein said processor is further arranged to convert said input data signal representing an image into a color-sequential output signal.
7. (Previously Presented) The digital video-processing unit of claim 1, wherein said processor is arranged to convert said input data signal representing an image into a sub-field modulated output signal for controlling the display unit.
8. (Original) The digital video-processing unit of claim 1, wherein a part of the memory means is arranged as a cyclic memory for storing a part of an image represented by the input data signal.
9. (Original) The digital video-processing unit of claim 1, wherein the memory manager has a further input for a second data input signal, and the memory manager is arranged to transfer the second data input signal to the memory means.
10. (Original) A video display system comprising a digital video-processing unit as claimed in claim 1, having an output for providing the output data signal and a

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display system coupled to the output of said digital video-processing unit for  
displaying of said output data signal.

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